



Advances in Analog and RF IC Design for Wireless Communication Systems: Chapter 9. High-Performance Pipelined ADCs for Wireless Infrastructure Systems

Michael Elliott, Boris Murmann

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This chapter provides an overview of design techniques and system aspects relevant to the application of high-speed pipelined ADCs in wireless base transceiver stations (BTS). The discussion begins with a derivation of typical ADC specifications for the receive path of a multi-carrier BTS system. Next, we investigate issues pertaining to the interface between the ADC and its driving circuitry, including complications that arise with sample-and-hold-amplifier-less (SHA-less) ADC frontends. Lastly, we summarize recent research results that look into the digital linearization of dynamic nonlinearities at the ADC's frontend.

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